

## Aircraft Maintenance Engineer Licence — Examination Subject 12 Avionics 2

### General

Civil Aviation Authority advisory circulars (ACs) contain information about standards, practices, and procedures that the Director has found to be an **acceptable means of compliance** with the associated rule.

Consideration will be given to other methods of compliance that may be presented to the Director. When new standards, practices, or procedures are found to be acceptable they will be added to the appropriate AC.

### Purpose

This AC provides an acceptable means of compliance with the syllabus content in respect of written examinations for Subject 12 (Avionics 2).

This AC also provides guidance material for recommended study material in respect of the examination syllabus in this AC.

### Related Rules

This AC relates specifically to Civil Aviation Rule Part 66 Subpart B — ‘Aircraft Maintenance Engineer Licence’.

### Change Notice

Revision 4 adds a note on the online application process and updates the ASPEQ email address in the section **Examination Overview: Subject 12**. It also corrects out of date references and adds a version history.

### Version History

The main changes are outlined below:

AC Revision No	Effective Date	Summary of changes
AC66-2.12, Rev 0	1 Dec 2008	Initial issue
AC66-2.12, Rev 1	29 June 2009	Removed the wording ‘multi choice’ (in respect of questions) from the Examination Overview Section.

AC66-2.12, Rev 2	10 May 2013	<p>Removed the wording 'Subject 12 (Avionics 2) is a 3 hour, closed book, written examination containing 100 questions' (in respect to length of examination) from the Examination Overview Section.</p> <p>Removed reference to an AME sample question booklet being available for purchase from ASL.</p>
AC66-2.12, Rev 3	10 March 2015	<p>Updated the web address for Aviation Services Limited (ASL).</p> <p>Deleted reference to an AME sample question booklet being available for purchase from ASL.</p> <p>Removed reference to examination length and number of questions.</p>
AC66-2.12, Rev 4	2 Oct 2023	<p>Adds a note on the online application process.</p> <p>Updates the ASPEQ email address in the section <b>Examination Overview: Subject 12.</b></p> <p>Corrects out of date references and minor formatting errors.</p> <p>Adds a version history.</p>

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## **Rule 66.53 Eligibility Requirements**

Rule 66.53(a)(2) requires an applicant for an AMEL to have passed written examinations, that are acceptable to the Director, relevant to the duties and responsibilities of an aircraft maintenance engineer in the category of licence sought.

The written examinations acceptable to the Director for Subject 12 (Avionics 2) should comply with the syllabus contained in this AC. Each examination will cover all topics and may sample any of the sub-topics.

The new syllabus has been developed after extensive industry consultation and the objectives reflect the knowledge required of current technology and international best work practice.

## Examination Overview: Subject 12

The pass mark for Subject 12 (Avionics 2) is 70%.

An application to sit an examination may be made directly to ASPEQ. Refer to <https://caanz.aspeqexams.com/home> for examination information.

### General Examining Objective

The objective of the examination is to determine that the applicant for an AMEL has adequate knowledge of Subject 12 to permit the proper performance, supervision and certification of aircraft maintenance at a level commensurate with the privileges of the various AMEL categories.

### Knowledge Levels

#### LEVEL 1: A familiarisation with the principal elements of the subject.

##### Objectives: The applicant should:

1. be familiar with the basic elements of the subject.
2. be able to give simple descriptions of the whole subject, using common words and examples.
3. be able to use typical terms.

#### LEVEL 2: A general knowledge of the theoretical and practical aspects of the subject.

##### *An ability to apply the knowledge.*

##### Objectives: The applicant should be able to:

1. understand the theoretical fundamentals of the subject.
2. give a general description of the subject using, as appropriate, typical examples.
3. use mathematical formulae in conjunction with physical laws describing the subject.
4. read and understand sketches, drawings and schematics describing the subject.
5. apply his/her knowledge in a practical manner using detailed procedures.

#### LEVEL 3: A detailed knowledge of the theoretical and practical aspects of the subject.

##### *A capacity to combine and apply the separate elements of knowledge in a logical and comprehensive manner.*

##### Objectives: The applicant should:

1. know the theory of the subject and the interrelationships with other subjects.
2. be able to give a detailed description of the subject using theoretical fundamentals and specific examples.
3. understand and be able to use mathematical formulae related to the subject.
4. be able to read, understand and prepare sketches, simple drawings and schematics describing the subject.
5. be able to apply his/her knowledge in a practical manner using manufacturer's instructions.
6. be able to interpret results and measurements from various sources and apply corrective action where appropriate.

**Note:** From October 2023, to add a new rating to your LAME licence, you can apply online for this through **MyAviation**, CAA's online portal for licensing requests, instead of filling in paper forms. Click the 'Online services' button on the CAA home page to get started.

## Recommended Study Material

The publication list below provides guidance material for suitable study references for the overall syllabus content. However, applicants may have to conduct further research using other references or sources (including the internet) or attend a formal course in order to gain a comprehensive understanding of all sub-topics in the syllabus.

Publication references have not been assigned to individual topics in this syllabus.

### Publication List

Study Ref	Book Title	Author	ISBN
1	Mechanics of Flight	AC Kermode	978-1-4058-2359-3
2	Aircraft Electricity & Electronics	TK Eismin	0-07-113286-4
3	Avionic Systems Operation & Maintenance	James W. Wasson	0-89100-436-X
4	Aircraft Engineering Principles.	L. Dingle & M. Tooley	0 7506 5015-X
5	Fibre Optics Communications and other Applications	Zanger	0-675-20944-7
6	Digital Electronic Systems	CR Spitzer	1-930665-12-1
7	Dictionary of Aeronautical Terms	Dale Crane	1-56027-287-2
8	Teach Yourself Electricity and Electronics	Stan Gibilisco	007-1377-301

## Syllabus Layout

### Topic Numbering – left hand column

The syllabus is set out by topics, each of which is identified by a single-digit number. Each topic is divided into a number of sub-topics, which are identified by two-digit numbers: the first and second digits of which refer to the topic and the sub-topic respectively.

Each sub-topic is further sub-divided into one or more sub-sub-topics, which are identified by three-digit numbers. Where applicable, sub-sub-topics may be further subdivided into paragraphs that are identified by four/five digit alphanumeric sequences.

The three-digit sub-sub-topic numbers shown in the left-hand column are used in the 'knowledge deficiency reports' to provide feedback on individual examinations.

### Objective description – middle column

The middle column objectively describes each sub-sub-topic by stating, in plain language, its subject matter and the type of performance or activity required. The objectives are intended to be simple, unambiguous, and clearly-focussed outcomes to aid learning.

### Knowledge levels – right hand column

The right-hand column specifies the knowledge level for each sub-topic heading. The three levels of knowledge used in this syllabus are described above. Note that the knowledge levels indicate the depth of knowledge required NOT its safety importance.



## Syllabus: Subject 12 (Avionics 2)

<b>1 High Speed Flight</b>		
1.1.1	Describe the following conditions and understand factors that affect them. <ul style="list-style-type: none"> <li>a. Critical Mach number</li> <li>b. Mach cone</li> <li>c. Mach number</li> <li>d. Speed of sound</li> <li>e. Subsonic flight</li> <li>f. Supersonic flight</li> <li>g. Transonic flight.</li> </ul>	2
1.1.2	Describe the following high speed flight criteria and know what factors affect them: <ul style="list-style-type: none"> <li>a. Aerodynamic heating.</li> <li>b. Area rule.</li> <li>c. Compressibility.</li> <li>d. Expansion waves.</li> <li>e. Shock induced drag.</li> <li>f. Shock induced stall.</li> <li>g. Shock waves (oblique and normal).</li> </ul>	2
1.1.3	Describe the airflow in engine intakes of high-speed aircraft and the factors which affect it.	2
1.1.4	Describe the effects of sweepback and fineness ratio on critical mach number.	2
1.1.5	Describe control problems encountered in transonic and supersonic flight and state the methods used to overcome these problems.	2
1.1.6	Describe aircraft structural monitoring (damage tolerance monitoring) systems and devices and the electrical systems that support them.	1

<b>2 Flight Controls</b>		
2.1.1	Describe the following flight control systems and devices: <ul style="list-style-type: none"><li>a. Trim control</li><li>b. Active load control</li><li>c. High lift devices</li><li>d. Lift dump</li><li>e. Speed brakes</li><li>f. Artificial feel</li><li>g. Yaw damper</li><li>h. Mach trim</li><li>i. Rudder limiter</li><li>j. Gust locks</li><li>k. Stall protection systems</li><li>l. Manual, hydraulic and pneumatic power.</li></ul>	1
2.1.2	Describe the construction, principles of operation, protection and maintenance of the following control systems: <ul style="list-style-type: none"><li>a. Digital fly-by-wire</li><li>b. Analogue fly-by- wire</li><li>c. Full authority systems</li><li>d. Manual reversion systems.</li></ul>	2

<b>3 Advanced AC Theory</b>		
<b>3.1</b>	<b>Resistive (R), Capacitive (C) and Inductive (L) Circuits</b>	
3.1.1	In relation to purely resistive, capacitive and inductive circuits: <ol style="list-style-type: none"> <li>a. Apply Ohm's Law to determine voltage, current and impedance</li> <li>b. Calculate inductive and capacitive reactance and state the factors that affect them</li> <li>c. Describe and calculate impedance and phase angle</li> <li>d. Describe the power dissipated and phase relationship between voltage and current.</li> </ol>	2
<b>3.2</b>	<b>Series and Parallel Inductive, Capacitive and Resistive Circuits</b>	
3.2.1	In relation to series and parallel L, C and R circuits, describe the relationship between: <ol style="list-style-type: none"> <li>a. Voltage across circuit components</li> <li>b. Current across circuit components.</li> </ol>	2
3.2.1	Calculate: <ol style="list-style-type: none"> <li>a. Applied and component voltage</li> <li>b. Current</li> <li>c. Impedance (Z)</li> <li>d. Phase angle</li> <li>e. Power factor.</li> </ol>	3
3.2.3	Describe and calculate: <ol style="list-style-type: none"> <li>a. True power (Watts)</li> <li>b. Apparent power</li> <li>c. Reactive power.</li> </ol>	3
<b>3.3</b>	<b>Series and Parallel Resonance</b>	
3.3.1	Define resonance.	2
3.3.2	Specify how the properties of a reactive circuit change at resonance.	2
3.3.3	When the frequency of a series resonant circuit is varied, describe the effect on: <ol style="list-style-type: none"> <li>a. Current</li> <li>b. Impedance</li> <li>c. Phase angle.</li> </ol>	2
3.3.4	From given information, calculate the resonant frequency.	3
3.3.5	Explain and calculate the voltage magnification factor (Q) of a circuit.	3
3.3.6	Describe the effect that resistance has on circuit Q and resonance curves.	2
3.3.7	Define bandwidth.	2
3.3.8	When the frequency of a parallel resonant circuit is varied, describe the effect on: <ol style="list-style-type: none"> <li>a. Current</li> <li>b. Impedance</li> <li>c. Phase angle.</li> </ol>	2
3.3.9	Describe the operation and use of a tank circuit.	2

<b>4 Transformers</b>		
<b>4.1</b>	<b>General</b>	
4.1.1	Describe the two classes of transformer and where each may typically be used in aircraft systems.	2
4.1.2	Describe the basic functions of a transformer in respect of AC voltage and frequency.	2
4.1.3	Describe the following components of a transformer: <ul style="list-style-type: none"> <li>a. Iron core</li> <li>b. Primary windings</li> <li>c. Secondary windings.</li> </ul>	2
4.1.4	Describe the principles of operation of a transformer.	2
4.1.5	Describe transformer losses and how they are overcome.	2
4.1.6	Describe the action of a transformer under load and no-load conditions.	2
4.1.7	Describe power transfer, efficiency and the relevance of polarity markings.	2
4.1.8	Given sufficient data calculate: <ul style="list-style-type: none"> <li>a. Efficiency</li> <li>b. Power</li> <li>c. Primary or secondary current</li> <li>d. Primary or secondary voltage</li> <li>e. Turns ratio.</li> </ul>	3
4.1.9	Describe the types and ratings of transformers that would normally be used to produce the required power source for various components and systems in an aircraft, such as: <ul style="list-style-type: none"> <li>a. Instrument lighting</li> <li>b. Instruments</li> <li>c. Servos</li> <li>d. Actuators</li> <li>e. Avionics equipment</li> <li>f. Pumps and motors</li> <li>g. Relays and valves</li> <li>h. Batteries.</li> </ul>	2
4.1.10	Identify the symbol for a transformer on an electrical circuit diagram.	2

<b>5 AC Power Systems</b>		
<b>5.1</b>	<b>Generation of AC</b>	
5.1.1	Describe waveform produced from a 360-degree rotation of a single loop in a magnetic field.	2
5.1.2	Describe the principle of operation and construction of the following types of AC generator: <ul style="list-style-type: none"> <li>a. Single revolving armature</li> <li>b. Revolving field.</li> </ul>	2
5.1.3	List the factors that affect the frequency and voltage output.	1
<b>5.2</b>	<b>Multiphase Waveform Analysis</b>	
5.2.1	In relation to star and delta connected 3-phase systems describe, and given sufficient data, calculate line and phase voltages and currents.	2
5.2.2	Calculate power in a 3-phase system.	3
5.2.3	Compare star and delta 3-phase systems.	2
<b>5.3</b>	<b>Filters</b>	
5.3.1	Describe the operation, uses and applications of the following types of filters. <ul style="list-style-type: none"> <li>a. Band pass</li> <li>b. Band stop</li> <li>c. High pass</li> <li>d. Low pass.</li> </ul>	2
5.3.2	Identify response curves applicable to the above filters.	2
5.3.3	Describe the operation of active filters.	2
5.3.4	Compare the difference between active and passive filters.	2
<b>5.4</b>	<b>Synchros, Resolvers and Other Input Devices</b>	
5.4.1	Describe the construction, operation and uses of the following synchro systems and components: <ul style="list-style-type: none"> <li>a. Capacitance transmitters</li> <li>b. Control transformers</li> <li>c. Differential</li> <li>d. E and I transformers</li> <li>e. Inductance transmitters</li> <li>f. Resolvers</li> <li>g. Linear Voltage Differential Transformers (LVDT).</li> </ul>	2

<b>5.5</b>	<b>Servomechanism Operation and troubleshooting</b>	
5.5.1	Define the following terms and how they relate to servomechanisms: <ul style="list-style-type: none"> <li>a. Analog</li> <li>b. Damping (various types)</li> <li>c. Deadband</li> <li>d. Feedback (various types)</li> <li>e. Follow up</li> <li>f. Hunting</li> <li>g. Null</li> <li>h. Servo motor</li> <li>i. Tacho generator.</li> </ul>	2
5.5.2	Describe the operation and components of a typical servomechanism.	2
5.5.3	Describe: <ul style="list-style-type: none"> <li>a. Displacement and rate control</li> <li>b. Displacement/displacement.</li> <li>c. Displacement/rate.</li> <li>d. Rate/displacement.</li> <li>e. Rate/rate.</li> </ul>	2
5.5.4	Given data and diagrams, determine the causes of unserviceabilities in servomechanisms.	3
5.5.5	Specify the causes and methods of rectifying hunting.	2
5.5.6	Describe the effects of reversing the leads on a synchro receiver.	2
<b>5.6</b>	<b>AC Measuring Instruments</b>	
5.6.1	Describe the operation, use and construction of the following instruments: <ul style="list-style-type: none"> <li>a. Ammeters</li> <li>b. Frequency meters</li> <li>c. Volt amps reactive (VAR) meters</li> <li>d. Voltmeters</li> <li>e. Watt meters.</li> </ul>	2

<b>6 Semiconductor Theory</b>		
<b>6.1</b>	<b>Semiconductor Materials</b>	
6.1.1	In relation to P and N semiconductor materials, describe: <ul style="list-style-type: none"> <li>a. Majority and minority carriers</li> <li>b. The effects of impurities on conduction</li> <li>c. The mechanism of electrical conduction in doped semiconductor material.</li> </ul>	2
6.1.2	Describe the PN junction in a semiconductor.	2
<b>6.2</b>	<b>PN Junction Operation</b>	
6.2.1	Trace the development of a potential barrier across a junction which is: <ul style="list-style-type: none"> <li>a. Forward biased</li> <li>b. Reverse biased</li> <li>c. Unbiased.</li> </ul>	1
6.2.2	Describe the following diode parameters: <ul style="list-style-type: none"> <li>a. Frequency</li> <li>b. Leakage current</li> <li>c. Maximum forward current</li> <li>d. Peak inverse voltage</li> <li>e. Power dissipation</li> <li>f. Temperature.</li> </ul>	2
<b>6.3</b>	<b>Diode Terminology and Identification, Function and Types</b>	
6.3.1	Describe terminology used and be able to analyse information supplied on diode data sheets and diagrams.	2
6.3.2	Describe the characteristic curves for the following diodes: <ul style="list-style-type: none"> <li>a. Germanium</li> <li>b. Ideal</li> <li>c. Silicon</li> <li>d. Zener.</li> </ul>	1
6.3.3	Identify and describe the operation of the following circuits in which diodes are used: <ul style="list-style-type: none"> <li>a. Bridge rectifiers (single and 3-phase)</li> <li>b. Clampers</li> <li>c. Clippers</li> <li>d. Full and half wave rectifiers</li> <li>e. Voltage doublers and triplers (multipliers).</li> </ul>	2
6.3.4	Specify how an ohmmeter is used to check a diode for serviceability.	2

6.3.5	Specify the function and applications of the following devices: <ul style="list-style-type: none"><li>a. Diacs</li><li>b. Light emitting diode (LED)</li><li>c. Photoconductive diode</li><li>d. Shottky diodes</li><li>e. Triacs</li><li>f. Varactor diode</li><li>g. Varistor.</li></ul>	2
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<b>6.4</b>	<b>Junction Transistors</b>	
6.4.1	Describe the characteristics and properties of a transistor.	2
6.4.2	Describe the operation of PNP and NPN transistors.	2
6.4.3	Define the terms: <ul style="list-style-type: none"> <li>a. Alpha</li> <li>b. Beta</li> <li>c. Distortion</li> <li>d. Saturation</li> <li>e. Frequency response</li> <li>f. <math>I_B</math></li> <li>g. <math>I_C</math></li> <li>h. <math>I_E</math></li> <li>i. Input impedance</li> <li>j. Output impedance</li> <li>k. Power gain</li> <li>l. <math>V_{be}</math>.</li> </ul>	2
6.4.4	Describe the process of amplification (current, voltage and power).	2
6.4.5	Describe the effects of temperature on transistors.	2
6.4.6	Interpret terminology and evaluate data supplied on transistor specification sheets.	3
6.4.7	Identify from supplied information, the emitter, base or collector terminals on various types of transistors.	2
6.4.8	State how an ohmmeter can be used to check the serviceability of a transistor.	1
<b>6.5</b>	<b>Types of Transistors and Transistor Configurations</b>	
6.5.1	Describe the biasing methods required to operate as a: <ul style="list-style-type: none"> <li>a. Class A amplifier.</li> <li>b. Class B amplifier.</li> <li>c. Class C amplifier.</li> <li>d. Switch.</li> </ul>	2
6.5.2	Specify uses and characteristics of the above amplifiers.	2
6.5.3	Describe simple transistorised circuits with the following: <ul style="list-style-type: none"> <li>a. Bias</li> <li>b. Decoupling</li> <li>c. Feedback</li> <li>d. Stabilisation.</li> </ul>	2
6.5.4	Describe the following methods of bias stabilisation: <ul style="list-style-type: none"> <li>a. Diode</li> <li>b. Negative feedback</li> <li>c. Temperature stabilisation resistor</li> <li>d. Thermistor</li> </ul>	2

	e. Transistor.	
6.5.5	Identify and describe the operation and relative characteristics of the following transistor configurations: a. Common base b. Common collector c. Common emitter.	2
6.5.6	Describe the operation, characteristics and application of the following devices: a. Hall effect devices b. Opto isolator c. Photo transistor d. Power transistors e. Unijunction transistors.	2
<b>6.6</b>	<b>Field Effect Transistors (FETs)</b>	
6.6.1	Outline the operation, characteristics and basic circuit configurations of the following FETs: a. Insulated gate (IGFET) b. Junction (JFET) c. Metal oxide silicon (MOSFET).	2

<b>7 Amplifiers and Circuits</b>		
<b>7.1</b>	<b>Operational Amplifiers</b>	
7.1.1	Specify the following parameters and their values for an ideal operational amplifier (OPAMP): <ul style="list-style-type: none"> <li>a. Bandwidth</li> <li>b. Drift</li> <li>c. Input and output impedance</li> <li>d. Input offset voltage and current</li> <li>e. Open loop gain</li> <li>f. Slew rate.</li> </ul>	2
7.1.2	Describe the operation of an OP AMP when used as a: <ul style="list-style-type: none"> <li>a. Comparator</li> <li>b. Differentiator</li> <li>c. Voltage follower</li> <li>d. Integrator.</li> </ul>	2
7.1.3	With respect to the following amplifiers, describe their operation and applications: <ul style="list-style-type: none"> <li>a. Differential amplifier</li> <li>b. Inverting amplifier</li> <li>c. Non-inverting amplifier.</li> <li>d. Summing amplifier.</li> </ul>	2
7.1.4	In regard to the above amplifiers, describe the effect of external components to determine gain and output voltage.	2
<b>7.2</b>	<b>Multistage Amplifiers</b>	
7.2.1	Identify and describe the operation of the following methods of connecting amplifier stages: <ul style="list-style-type: none"> <li>a. Direct</li> <li>b. Inductive (transformer)</li> <li>c. Inductive resistive (IR)</li> <li>d. Inductive capacitive (LC)</li> <li>e. Resistive capacitive (RC).</li> </ul>	2
7.2.2	Compare the advantages and disadvantages of the methods listed above.	2
7.2.3	Identify positive and negative feedback, list the advantages and disadvantages of each and describe their applications.	2
7.2.4	Identify the cause of oscillation in an amplifier.	2
<b>7.3</b>	<b>Practical Transistorised Circuits</b>	
7.3.1	List the characteristics and describe the operation of the following devices: <ul style="list-style-type: none"> <li>a. Complementary symmetry configuration</li> <li>b. Darlington pairs</li> <li>c. Push-pull amplifiers</li> <li>d. Cascade amplifier.</li> </ul>	2
	From given circuit diagrams, assess the serviceability of transistors and related devices.	2

<b>7.4</b>	<b>Multivibrators and Oscillators</b>	
7.4.1	Describe the operation and state the characteristics of the following types of multivibrators: <ul style="list-style-type: none"> <li>a. Astable or free running.</li> <li>b. Bistable or flip flop.</li> <li>c. Monostable or one shot.</li> </ul>	2
7.4.2	Identify and describe the operation of the following oscillators: <ul style="list-style-type: none"> <li>a. Colpitts</li> <li>b. Crystal</li> <li>c. Hartley</li> <li>d. Inductive capacitive (LC)</li> <li>e. Resistive capacitive (RC).</li> </ul>	2
<b>7.5</b>	<b>Signal Processing Devices</b>	
7.5.1	Explain the basic operation and uses of the following processing devices: <ul style="list-style-type: none"> <li>a. Demodulators</li> <li>b. Limiters</li> <li>c. Modulators</li> <li>d. Summing networks or points.</li> </ul>	3
<b>7.6</b>	<b>Printed Circuit Boards</b>	
7.6.1	Describe the construction, use, maintenance and testing of printed circuit boards.	2
7.6.2	Outline typical installation and removal procedures for printed circuit boards.	2
7.6.3	Diagnose common defects found in printed circuit boards.	3
7.6.4	Diagnose faults using circuit diagrams.	3

<b>8 Digital Techniques</b>		
<b>8.1</b>	<b>Digital Arithmetic Computation</b>	
8.1.1	Describe and compare the binary and decimal numbering systems.	2
8.1.2	Convert decimal into binary and vice versa.	2
8.1.3	Calculate the addition of two or more binary numbers.	2
8.1.4	Convert decimal into octal and hexadecimal, and vice versa.	2
8.1.5	Show addition and subtraction in a binary coded decimal (BCD) and hexadecimal form.	2
<b>8.2</b>	<b>Logic Gates and Circuits</b>	
8.2.1	Distinguish between: <ul style="list-style-type: none"> <li>a. Logic circuits</li> <li>b. Linear circuits.</li> </ul>	2
8.2.2	Evaluate logic diagrams.	3
<b>8.3</b>	<b>Flip Flop Terminology, Operation and Application</b>	
8.3.1	In relation to flip flops, define the following: <ul style="list-style-type: none"> <li>a. Asynchronous input</li> <li>b. Maximum clocking frequency</li> <li>c. Propagation delay</li> <li>d. Set-up and hold times</li> <li>e. Synchronous input</li> <li>f. Transition (positive and negative)</li> <li>g. Toggling.</li> </ul>	2
8.3.2	Identify the symbols used to indicate clocked inputs and negative going transition.	2
8.3.3	Describe the operation of and identify the symbols and truth tables for the following types of flip flops: <ul style="list-style-type: none"> <li>a. D</li> <li>b. JK</li> <li>c. SC or RS.</li> </ul>	2
8.3.4	Describe the operation of digital: <ul style="list-style-type: none"> <li>a. Counters (including synchronous and semi-synchronous)</li> <li>b. Data storage devices</li> <li>c. Shift registers.</li> </ul>	2
8.3.5	In relation to serial and parallel data transfer: <ul style="list-style-type: none"> <li>a. Describe how each is generated and transferred from one to the other (UART).</li> <li>b. Describe the uses; and list the advantages and disadvantages of each.</li> </ul>	2

<b>8.4</b>	<b>Encoders and Decoders</b>	
8.4.1	Specify the operation and uses of encoders and decoders.	2
8.4.2	Identify the following types of encoders and decoders: <ul style="list-style-type: none"> <li>a. Binary coded decimal (BCD) to decimal or (4 to 10)</li> <li>b. Decimal to BCD</li> <li>c. Binary to octal or (1 to 8)</li> <li>d. Octal to binary or (8 to 3)</li> <li>e. Hex to binary</li> <li>f. Binary to hex</li> <li>g. Decimal to binary</li> <li>h. Binary to decimal</li> <li>i. Priority encoders.</li> </ul>	2
<b>8.5</b>	<b>Displays</b>	
8.5.1	Describe the operation of the following types of displays: <ul style="list-style-type: none"> <li>a. Gas discharge.</li> <li>b. Light emitting diodes (LED).</li> <li>c. Liquid crystal.</li> <li>d. Plasma displays.</li> </ul>	2
8.5.2	Describe the operation and use of a typical BCD to decimal and BCD to seven segment decoder drivers.	2

<b>9 Computers and Electronic Devices</b>		
<b>9.1</b>	<b>Data Conversion</b>	
9.1.1	<p>In relation to analogue to digital and digital to analogue converters, be able to:</p> <ol style="list-style-type: none"> <li>a. Define terminology relating to them</li> <li>b. Describe the operation and limitations of various types</li> <li>c. State their inputs, outputs and factors affecting them</li> <li>d. State their purpose and function.</li> </ol>	2
<b>9.2</b>	<b>Computer Related Terminology</b>	
9.2.1	<p>Define the following terms:</p> <ol style="list-style-type: none"> <li>a. Accumulator</li> <li>b. Address</li> <li>c. Assembly language programme</li> <li>d. Bit</li> <li>e. Byte</li> <li>f. CPU (Central Processor Unit)</li> <li>g. Firmware</li> <li>h. Instruction</li> <li>i. Instruction word</li> <li>j. Label</li> <li>k. Language</li> <li>l. Machine language</li> <li>m. Mnemonic</li> <li>n. Op code</li> <li>o. Operand</li> <li>p. Software</li> <li>q. Word.</li> </ol>	2
<b>9.3</b>	<b>Microprocessors</b>	
9.3.1	List the functions performed and overall operation of a microprocessor.	1
<b>9.4</b>	<b>Basic Microcomputers</b>	
9.4.1	Outline the layout, basic operation and interfacing of the major components of a microcomputer including their associated bus system.	2
9.4.2	Explain the information contained in single and multi-address instruction words.	2

<b>9.5</b>	<b>Memory Devices</b>	
9.5.1	Define the following memory associated terms: <ul style="list-style-type: none"> <li>a. Access time.</li> <li>b. Capacity</li> <li>c. Cycle time</li> <li>d. Memory cell</li> <li>e. Memory word</li> <li>f. Read operation</li> <li>g. Write operation</li> <li>h. RAM</li> <li>i. ROM</li> <li>j. PROM</li> <li>k. EPROM</li> <li>l. Volatile</li> <li>m. Non-volatile.</li> </ul>	2
9.5.2	Describe the operation of a typical memory device during READ and WRITE operations.	2
9.5.3	Define the following terms associated with the operation of PROMs: <ul style="list-style-type: none"> <li>a. Burning</li> <li>b. Erased</li> <li>c. Non-volatile.</li> </ul>	2
9.5.4	Describe the operation of the chip select line in order to perform the read operation.	2
9.5.5	Describe: <ul style="list-style-type: none"> <li>a. The difference between static and dynamic RAMs</li> <li>b. Advantages and disadvantages between static and dynamic RAMs</li> <li>c. The use and operation of RAMs</li> <li>d. The reasons for dynamic RAM refreshing and how this is accomplished.</li> </ul>	2
<b>9.6</b>	<b>Multiplexers, Demultiplexers and Tristate Devices</b>	
9.6.1	Describe the principles of time and frequency division multiplexing.	2
9.6.2	Specify the uses and operation of: <ul style="list-style-type: none"> <li>a. Multiplexers</li> <li>b. Demultiplexers</li> <li>c. Tristate devices.</li> </ul>	2
9.6.3	Identify their symbols and logic diagrams.	2
<b>9.7</b>	<b>Codes</b>	
9.7.1	Describe the following codes and be able to convert binary and decimal numbers into them: <ul style="list-style-type: none"> <li>a. BCD</li> <li>b. Grey.</li> </ul>	2
9.7.2	Outline the structure and uses of the ASCII code.	2
9.7.3	Describe the parity method of error detection.	2



<b>9.8</b>	<b>Electrostatic Sensitive Devices (ESD)</b>	
9.8.1	Specify the sources of ESD and the damage that can occur.	2
9.8.2	Describe the special handling, identification packaging and protection requirements.	2
9.8.3	Identify ESDs <del>sensitive devices</del> .	2
9.8.4	Identify dangerous situations for generating static charge build-ups.	2
9.8.5	Describe component and personal anti-static protection devices.	2
<b>9.9</b>	<b>Electromagnetic Environment</b>	
9.9.1	Describe the following phenomena on maintenance practices for electrical systems: <ul style="list-style-type: none"> <li>a. EMC – Electromagnetic Compatibility</li> <li>b. EMI – Electromagnetic Interference</li> <li>c. HIRF – High Intensity Radiated Field</li> <li>d. Lightning and lightning protection.</li> </ul>	2
<b>9.10</b>	<b>Data Buses</b>	
9.10.1	Describe the operation of different data bus systems as they apply to aircraft avionic systems. These systems include but not limited to the following: <ul style="list-style-type: none"> <li>a. ARINC 429</li> <li>b. ARINC 629</li> <li>c. ASCB (Avionics Standard Data Bus)</li> <li>d. CSBD (Commercial Standard Data Bus)</li> <li>e. MIL-STD 1553</li> <li>f. MIL-STD 1773</li> <li>g. Ethernet local area network</li> <li>h. Token ring local area network.</li> </ul>	2

<b>9.11</b>	<b>Optical Theory and Fibre Optics</b>	
9.11.1	Compare the advantages and disadvantages of fibre optical data transmission over electrical wire.	2
9.11.2	Define the following terms and effects relating to fibre optics: <ul style="list-style-type: none"> <li>a. Absorption</li> <li>b. Active medium</li> <li>c. Black body</li> <li>d. Coherent bundle</li> <li>e. Coherent light</li> <li>f. Diffraction</li> <li>g. Dispersion</li> <li>h. Flux rise time</li> <li>i. Multimode fibres</li> <li>j. Optical attenuators</li> <li>k. Two state modulation.</li> </ul>	2
9.11.3	Describe procedures and safety precautions to be observed during optical fibre cable terminations with particular regard to the following: <ul style="list-style-type: none"> <li>a. Cleaving</li> <li>b. Stripping</li> <li>c. Splicing</li> <li>d. Termination losses.</li> </ul>	2
9.11.4	Describe the following devices: <ul style="list-style-type: none"> <li>a. Couplers</li> <li>b. Control terminals</li> <li>c. Remote terminals.</li> </ul>	2
9.11.5	Describe optical fibre data bus transmissions – transmissions and receiving of information.	2
9.11.6	Define what is meant by a “Collimated Light Source”, when this light source would be used, and the advantages of using one.	1
9.11.7	Describe the following topology: <ul style="list-style-type: none"> <li>a. Passive star</li> <li>b. Active star</li> <li>c. Transmissive star.</li> </ul>	2
9.11.8	Specify the application of fibre optics in aircraft systems.	2
<b>9.12</b>	<b>Software Management Control</b>	
9.12.1	Specify the design requirements for aircraft software.	2
9.12.2	Specify the necessary restrictions, airworthiness requirements and possible catastrophic effects of unapproved modifications or alterations to manufacturer’s software programmes.	2

<b>10 Master and Central Warning Systems</b>		
10.1.1	Describe the operation and indications (oral and visual) provided by typical master and central warning systems including inputs, output warnings and priority philosophy.	2
10.1.2	Describe the layout and location of a typical annunciator panel.	2
10.1.3	Identify the units and systems that would typically be monitored by a central warning system.	3

<b>11 Electrical Diagrams and Identification Schemes</b>		
11.1.1	Understand thoroughly the symbols, terminology, coding systems and other information contained on a: a. Circuit diagram b. Wiring diagram c. Routing chart.	3
11.1.2	Describe the operating mode (or state) that relays or contactors would normally be shown in on a circuit diagram.	3
11.1.3	Interpret the data contained in ATA 100 codes for cable installations.	3

<b>12 Soldering</b>		
12.1.1	Explain the chemical combination of solder and the effect that varying the tin/lead content has on the melting point.	3
12.1.2	Describe the types, uses and purpose of flux and the reason why flux must be removed after soldering.	2
12.1.3	Define tinning. Describe the result excessive tinning has on wire, state the tool used to prevent it and why soldering iron tips are tinned.	2
12.1.4	Define the differences between soft and silver soldering and know the advantage of using either.	2
12.1.5	Identify cold joints.	3
12.1.6	Describe the reasons for and the uses of heat sinks, thermal shunts and solder removing devices (suckers, braid).	2
12.1.7	Describe the correct amount of wrapping to be used on various types of terminals.	2
12.1.8	Describe the maintenance of soldering irons and soldering tools.	2

<b>13 Cable Connection</b>		
13.1.1	Be able to identify the various aspects of an electrical connector by its identification code.	2
13.1.2	Describe the components of a typical British type plug and socket assembly.	3
13.1.3	Describe the following American type connectors and where each would typically be used: <ul style="list-style-type: none"> <li>a. Bulkhead receptacle</li> <li>b. Cable receptacle</li> <li>c. Box receptacle</li> <li>d. Quick disconnect plug</li> <li>e. Straight plug</li> <li>f. Bulkhead plug.</li> </ul>	2
13.1.4	Describe the methods used and precautions associated with attaching connectors to coaxial cables.	2
13.1.5	Describe the procedure for the complete replacement of an electrical connector. List the tools required (removal/insertion tools and/or appropriate soldering tool).	2
13.1.6	Describe the "potting" technique for connector sealing.	2
13.1.7	Describe the following types of crimp end and give examples of where each may be used: <ul style="list-style-type: none"> <li>a. Inline</li> <li>b. Lug</li> <li>c. Bayonet</li> <li>d. Twist joint</li> <li>e. Blind end</li> <li>f. Terminal.</li> </ul>	3
13.1.8	Describe the following factors relating to the identification of crimp joints: <ul style="list-style-type: none"> <li>a. Colour code</li> <li>b. Identification marks</li> <li>c. Insulation grip</li> <li>d. Crimp form.</li> </ul>	2
13.1.9	Assess a crimped joint for serviceability.	3
13.1.10	Describe the following tests relating to crimp joints: <ul style="list-style-type: none"> <li>a. Millivolt drop test</li> <li>b. Crimp pull test.</li> </ul>	2
13.1.11	Describe the use and precautions regarding hand operated crimping tools.	2
13.1.12	Describe the following factors relating to crimping tools: <ul style="list-style-type: none"> <li>a. Types</li> <li>b. Colour codes</li> <li>c. Ratchet devices</li> <li>d. Jaws and chucks</li> <li>e. Testing</li> <li>f. Go and no-go gauges.</li> </ul>	2

13.1.13	Describe the use of copper and aluminium cable connections with particular regard to the following criteria: <ul style="list-style-type: none"><li>a. Interchangeability copper and aluminium</li><li>b. Advantages and disadvantages of each type</li><li>c. Corrosion protection of aluminium</li><li>d. Pre-sealing</li><li>e. Filling of the cartridge</li><li>f. Use of flat washers for aluminium connections.</li></ul>	2
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